System-Level Programming

31 Concurrent Threads

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process creation, termination and, switching are expensive!

For **practical applications**, we therefore should take into account:

In multiprocessor systems physically parallel procedures are possible

- only few processes should be created/terminated
- never create more processes than there are physical processorsor

instead of expensive processes use simple threads



- Each **thread** represents its own active procedure
 - individual program counter
 - individual set of registers
 - individual stack (for local variables)
- Shared execution environment provides a set of resources
 - memory mapping
 - permissions
 - open files
 - root and working directory
 - ..



- The concept of a process is split up into one **execution environment** and one or more **threads**
- A classical UNIX process is a thread in an execution environment



- Switching between threads inside one process is also cheaper than switching between processes
 - only the registers and the program counter have to be changed (similar to a function call)
 - memory mapping does not have to be changed (cached content stays valid!)



31-Threads

- Threads work concurrent/parallel and have shared memory => all problems occurring when dealing with signals and interrupts and accessing shared data do also occur here
- Difference between threads and ISRs/signal handling functions:
 - "main thread" of an application and an ISR/signal handling function are not equalised
 - ISR/signal handler function interrupts the main thread but they don't get interrupted themselves
 - two threads are equalised
 - a thread can always be interrupted in favor of an other thread by the scheduler or be run in parallel to another one (MPS)
 - => It does not suffice to block signals!



mutual exclusion (coordination)

Example:

A thread wants to read a set of data and prevent other threads from changing the data in this time.

mutual waiting (synchronization)

Example:

A thread waits for an other thread so that they can combine partial results that each thread has calculated.



bounded buffer:

Threads write data into a buffer, others remove data from it; critical situations:

- access to the buffer
- buffer empty/full

Inserting an element:

- wait until there is free space
- wait until no other thread reads/writes from/to the buffer
- write into the buffer
- send signal that there is a new element in the buffer

Removing an element:

- wait until an element is in the buffer
- wait until no other thread reads/writes
- read from the buffer
- send signal that there is free space in the buffer

```
volatile int m = 0; /* 0: free; 1: locked */
volatile int counter = 0;
                                                /* Thread 2 */
          /* Thread 1 */
                                      lock(&m):
lock(&m):
                                      printf("%d\n", counter);
counter++;
                                      counter = 0:
unlock(&m):
                                      unlock(&m);
. . .
                                      . . .
```

Only the thread that called lock is allowed to call unlock!

Realization (only conceptual!)

```
void lock(volatile int *m) {
                                    void unlock(volatile int *m) {
    while (*m == 1) {
                                        *m = 0:
        /* Wait... */
    *m = 1;
```



lock (and unlock) have to be executed atomically! System-Level Programming (ST 24)

1-Threads_en

- A semaphore (greek. character carrier) is a data structure with two instructions (refer *Dijkstra*):
 - P-operation (proberen; passeren; wait; down)

```
void P(volatile int *s) {
   while (*s <= 0) {
        /* Wait/sleep... */
   }
   *s -= 1;
}</pre>
```

■ V-operation (verhogen; vrijgeven; signal; up)

```
void V(volatile int *s) {
    *s += 1;
    /* Wakeup... */
}
```

P and V have to be executed atomically!

P and V do not have to be called from the same thread.



```
#define N 1000
volatile int mutex = 0;
volatile int alloc = 0. free = N:
volatile int head = 0, tail = 0;
volatile int buf[N];
```

Inserting element:

```
void put(int x) {
    P(&free);
    lock(&mutex);
    buf[head] = x;
    head = (head + 1) % N;
    unlock(&mutex);
    V(&alloc);
```

Removing element:

```
int get(void) {
    int x:
    P(&alloc):
    lock(&mutex);
    x = buf[tail];
    tail = (tail + 1) % N;
    unlock(&mutex);
    V(&free);
    return x;
```

Spin Lock vs. Sleeping Lock

Spin lock

- active waiting until mutex variable is free (= 0)
- conceptually similar to polling
- thread stays in the state "running"

Problem: when there is only *one processor available*, computation time is wasted until the scheduler schedules a switch

only another running thread can free the mutex variable

Sleeping Lock

- passive waiting
- thread changes state to "blocked"
- when unlock occurs, the blocked thread changes to the state "ready"

Problem: for really short critical sections the expenses for blocking/waking up and switching are disproportionately expensive



Main problem: atomicity of mutex request and setting

```
void lock(volatile int *m) {
    while (*m == 1) {
         /* Wait... */
                                          critical section
    *m = 1:
```

- Solution: special machine instructions that enable to atomically request and modify a cell in the main memory
 - Test-and-Set, Compare-and-Swap, Load-Link/Store-Conditional, ...



1. Conflict with a second lock operation:

Atomicity of mutex request and setting

2. Conflict with second unlock: lost-wakeup problem

```
void lock(volatile int *m) {
     while (*m == 1) {
         sleep();
     }
     *m = 1;
}
```

Scenarios:

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- 1. switching of processes during a lock operation
- 2. truly parallel running lock- and/or unlock operations

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Prevent process switches

- process switches are functions of the OS kernel
 - takes place in the context of system calls (e.g., exit)
 - or on the context of an interrupt handler (e.g., time disc interruption)
 - => lock/unlock are implemented in the OS kernel as well; OS kernel with interrupt lock

```
void lock(volatile int *m)
{
    enter_OS();
    cli();
    while (*m == 1) {
        block_thread_and_schedule();
    }
    *m = 1;
    sei();
    leave_OS();
}
```

```
void unlock(volatile int *m)
{
    enter_OS();
    cli();
    *m = 0;
    wakeup_waiting_threads();
    sei();
    leave_OS();
}
```



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Resolve scenario (2):

Prevent parallel execution on an other processor

```
void lock(volatile int *m)
    enter_OS();
    cli():
    spin_lock();
    while (*m == 1) {
        block thread and schedule():
    *m = 1:
    spin_unlock();
    sei():
    leave_OS();
```

```
void unlock(volatile int *m)
    enter_OS();
    cli();
    spin_lock();
    *m = 0:
    wakeup_waiting_threads();
    spin_unlock();
    sei();
    leave_OS();
```

P() and V() similar

