

# CO<sub>2</sub>CoDe: Towards Carbon-Aware Hardware/Software Co-Design for Intermittently-Powered Embedded Systems

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July 9th, 2024

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Friedrich-Alexander-Universität Erlangen-Nürnberg



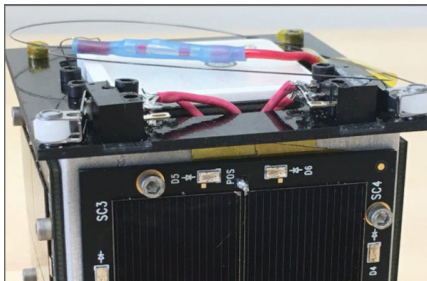
Lehrstuhl für Informatik 4  
Systemsoftware



Friedrich-Alexander-Universität  
Faculty of Engineering



Project no 502947440 (Watwa)  
Project no 502615015 (ResPECT)



🌐 Lucia, ACM SIGARCH Blog, 2022



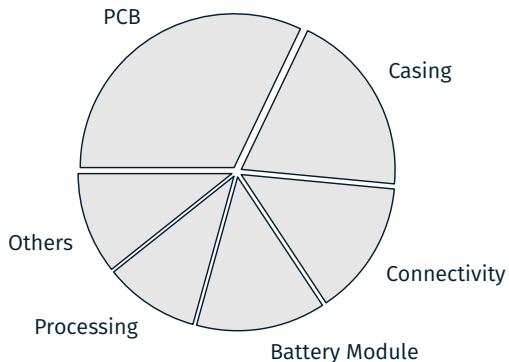
🌐 PR Ganapathy, CC BY-SA 4.0, 2015



📖 Afanasov, SenSys, 2020

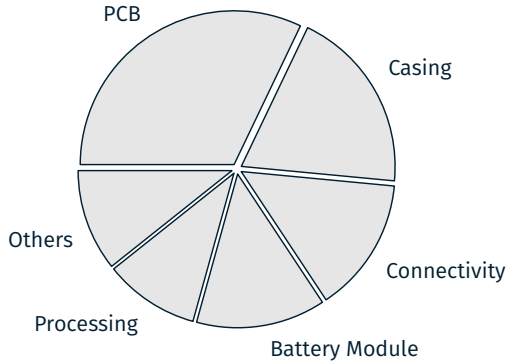


📖 de Winkel, MobiSys, 2022



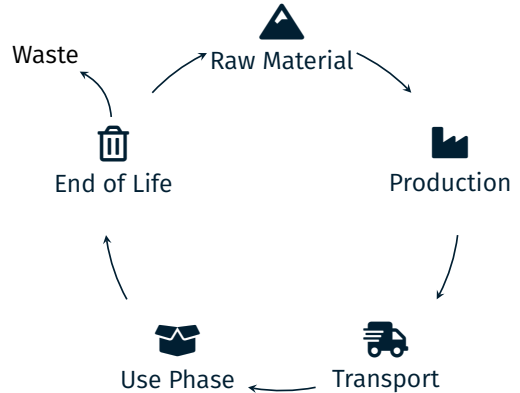
**Breakdown of the Global Warming Potential  
of a Mobile Device [kgCO<sub>2</sub>]**

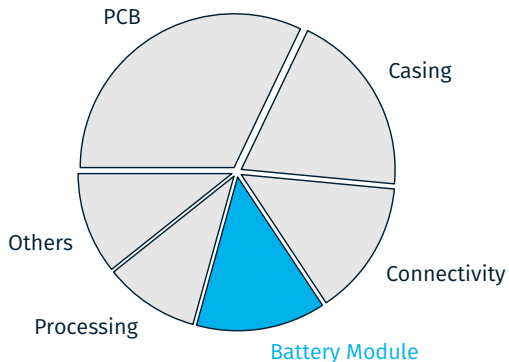
after  Maistriaux, IOT, 2022



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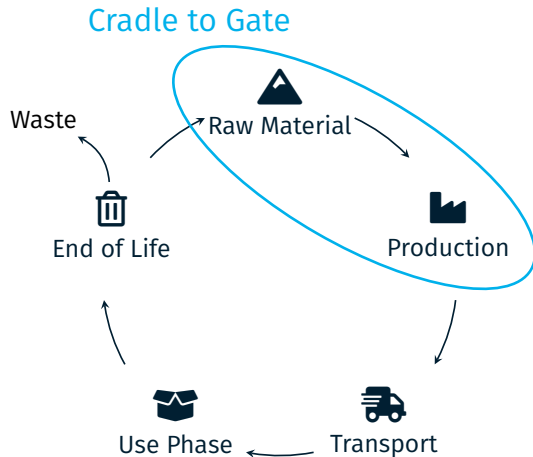
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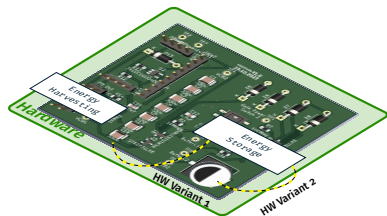




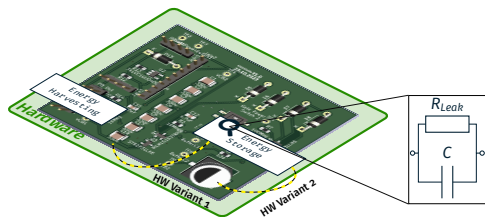
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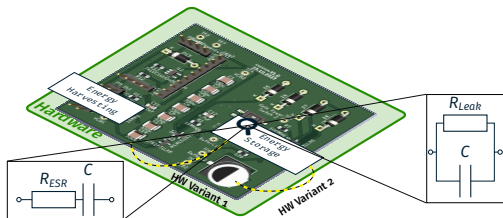




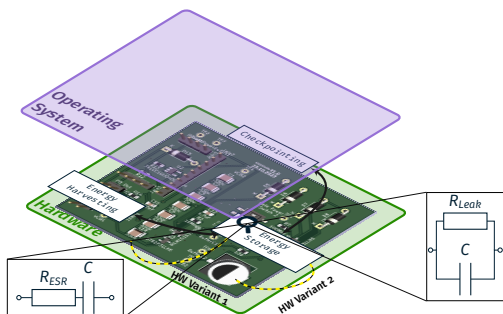
- Leakage resistance



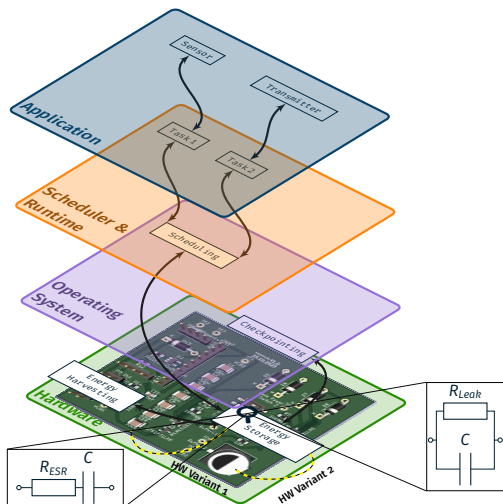
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- Equivalent Series Resistance (ESR)



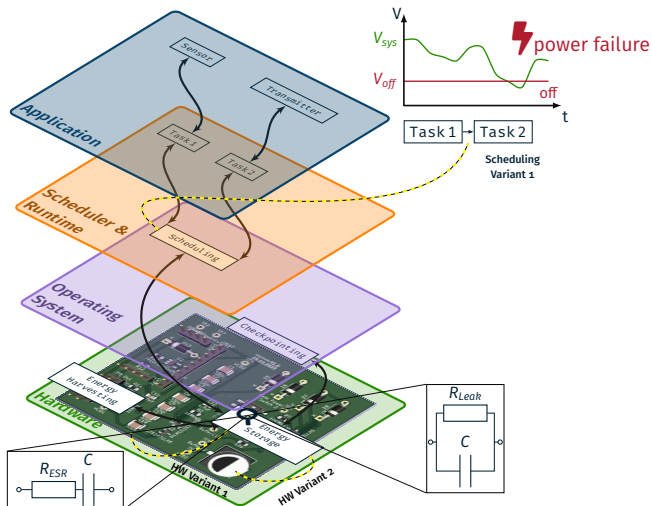




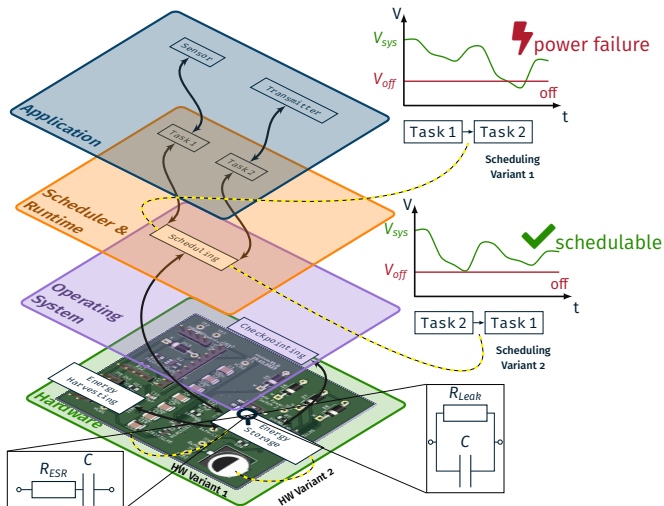
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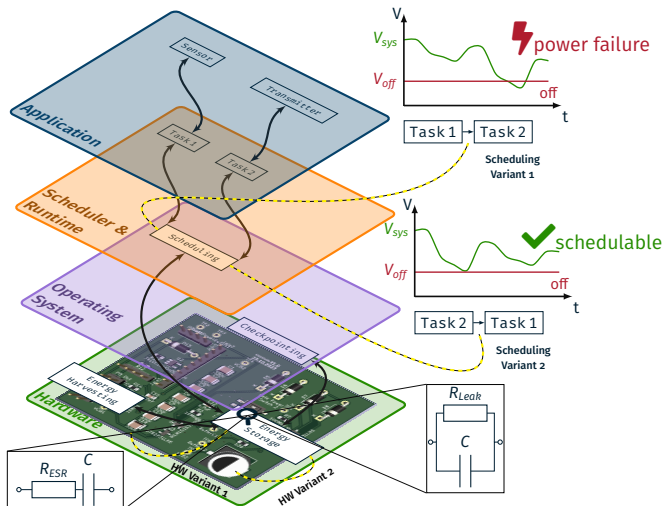
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- Effects propagate throughout the system stack



## Problems

- Propagation throughout whole system stack
  - Hardware  $\mapsto$  Software
  - Software  $\mapsto$  Hardware
- Multi-objective design choices
- Lack of proper abstractions

## Goals

- HW/SW co-design framework for carbon-conscious design
- Achieve sustainable designs of battery-free systems

## ***CO<sub>2</sub>CoDe* Case Study**

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## Multiple Objectives

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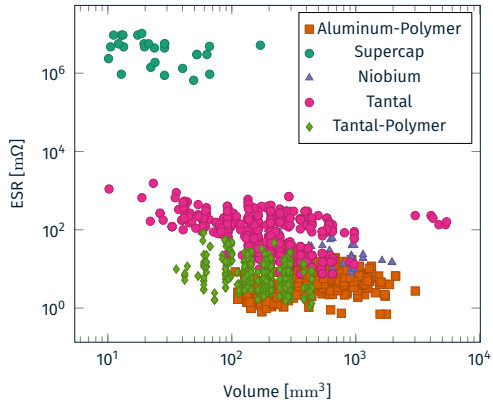
## Maximizing Available Energy Through Task Schedules

- Order tasks while taking maximum power demand into account
- ⇒ Diminish impacts of ESR-related voltage drop

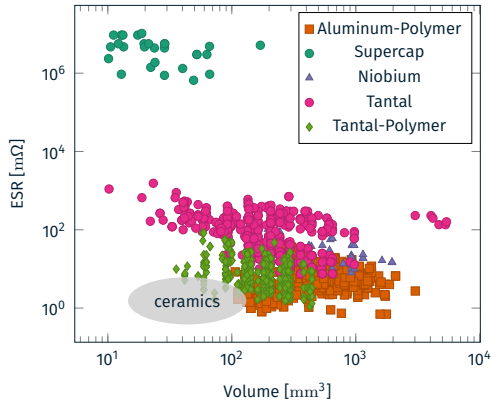
# Results

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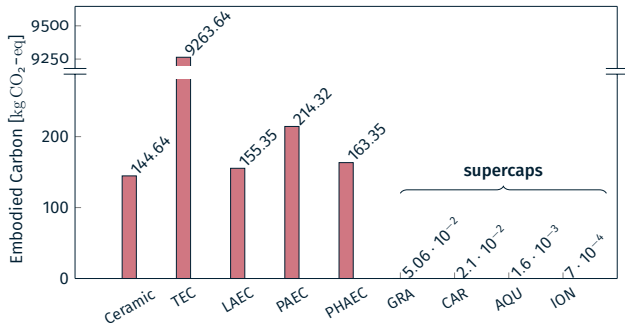
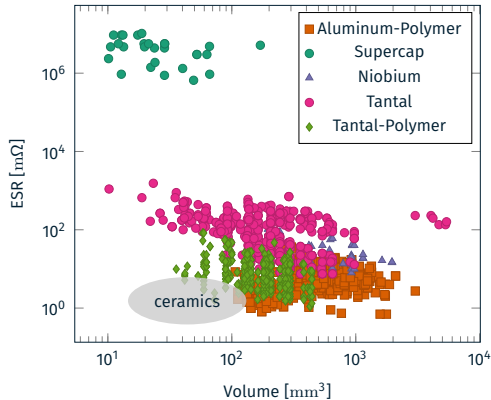
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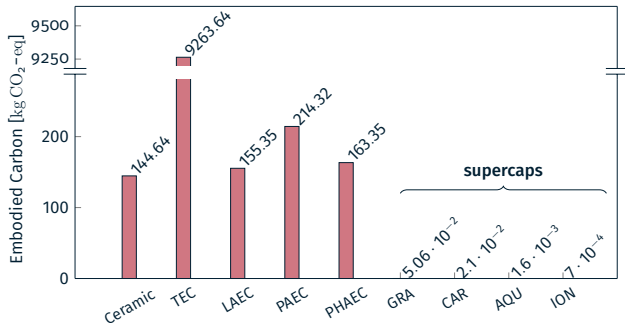
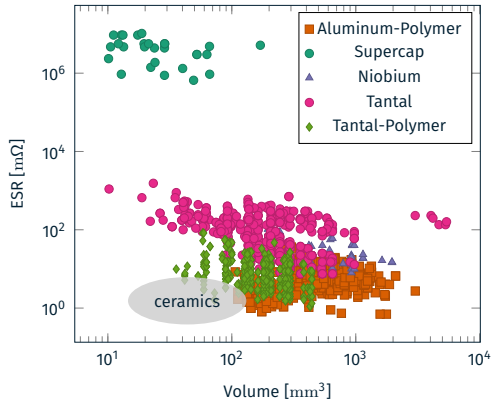
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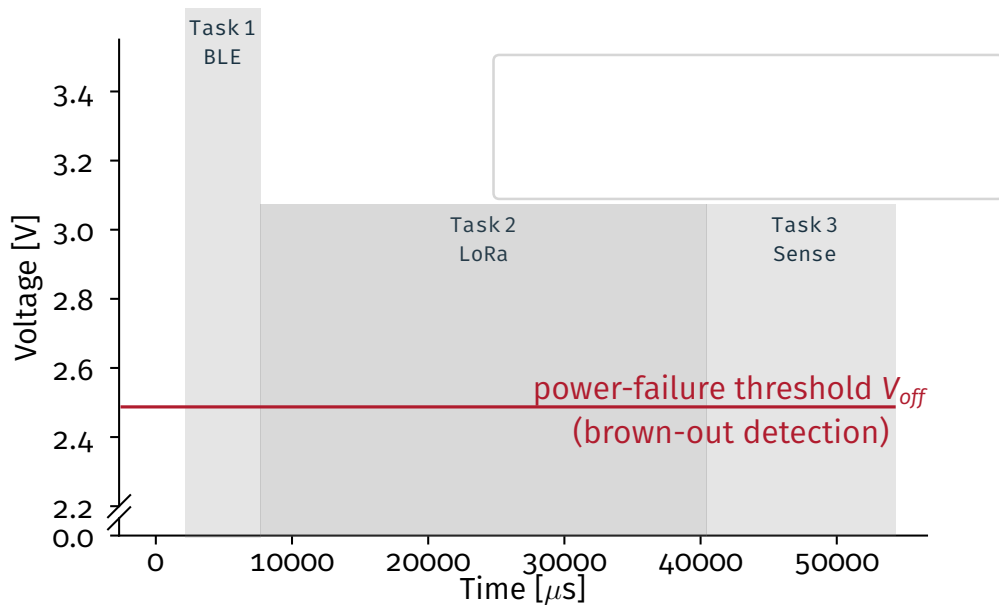


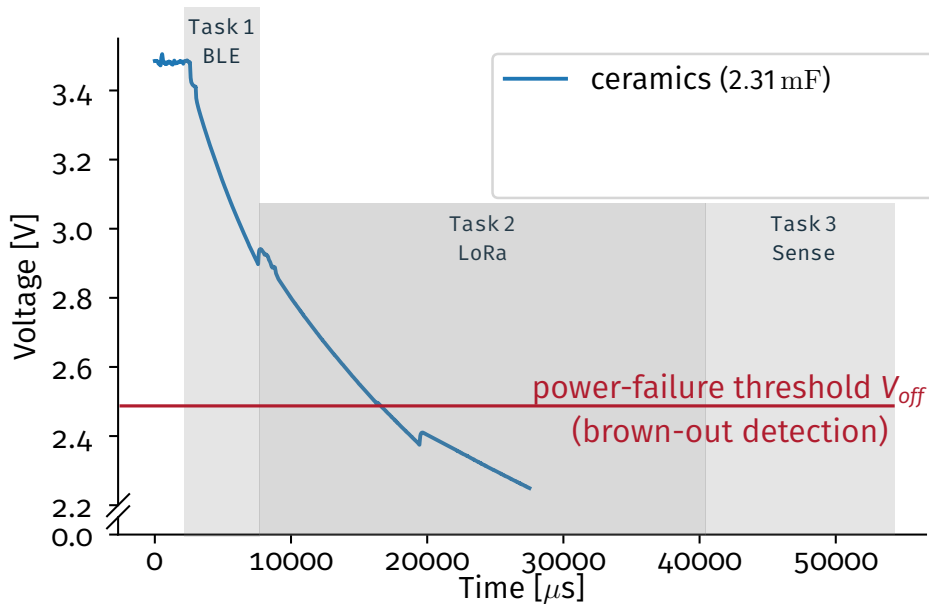




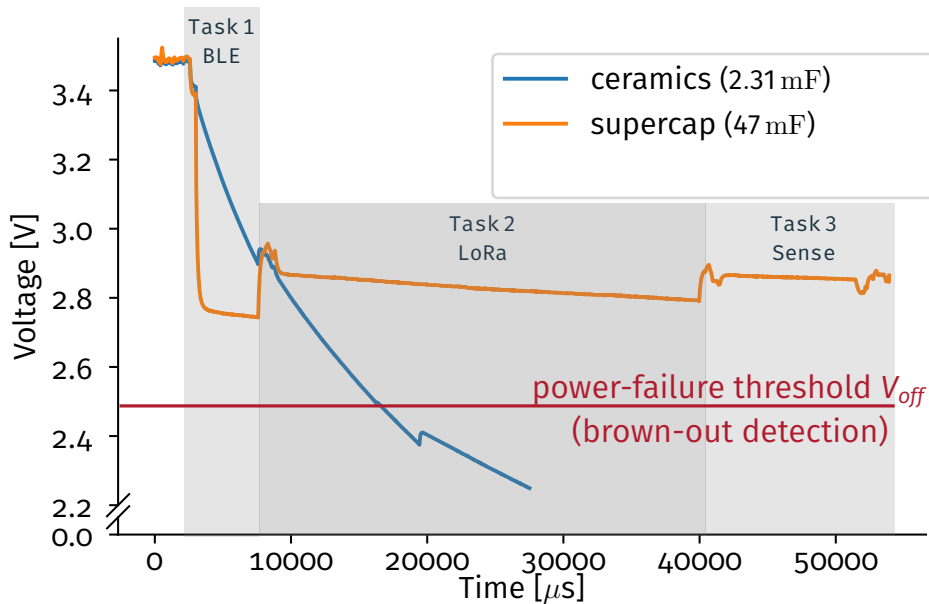
## Chosen Capacitors: ceramics and supercaps

- Low carbon footprint
- Ceramics: low ESR but low energy density
- Supercaps: high energy density but high ESR

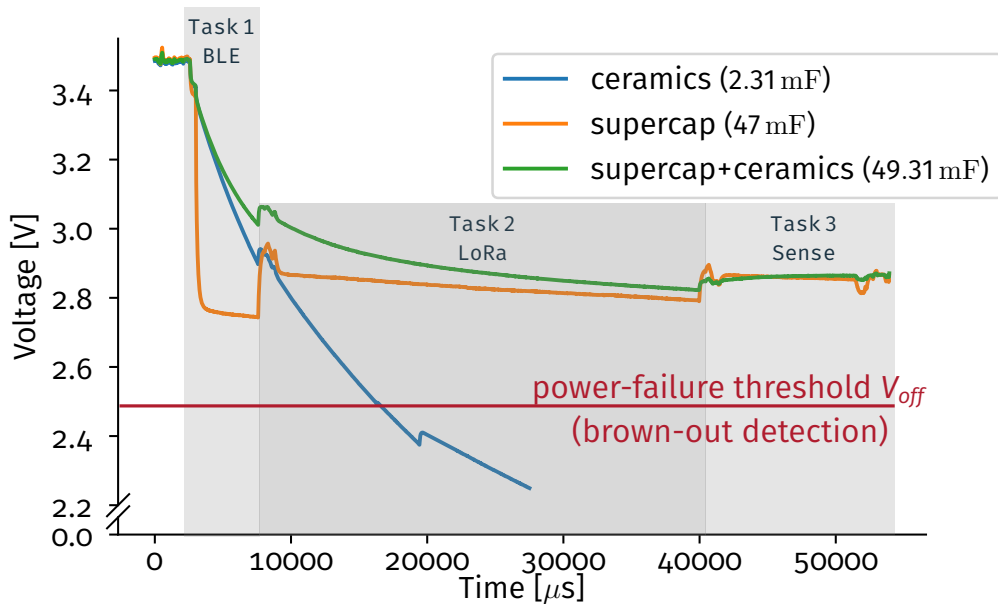




# Voltage Trace



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# Conclusion

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## Recap

- Multi-objective, **contradicting** design choices
- Current lack of proper abstractions
- Case study: capacitor selection with **conflicting properties**
- Energy-storage design **propagates** through entire system stack

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## Future Work

- Create a hardware/software co-design framework for carbon-conscious design decisions
- Bridge gap between system-level functionality and carbon awareness
- Sustainable design of carbon-aware and battery-free systems

*CO<sub>2</sub>CoDe's* project repository: <https://gitos.rrze.fau.de/co2code>